



[8086 Disassembler Download Zombies](#)

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0x1F49 10 27 00 B2 LBEQ $1FFF (_coco3v_text_putc+0xC2)// long branch if equal (rel_word)
0x1F4D func: _coco3v_text_putc line: coco3_video.c(91):
    else if (c == '\r')
0x1F4D C1 0D CMPB #$0D // compare b (imm_byte)
0x1F4F 26 06 BNE $1F57 (_coco3v_text_putc+0x1A)// branch if not equal (rel_byte)
0x1F51 func: _coco3v_text_putc line: coco3_video.c(93):
    g_cursor_x = 0;
0x1F51 7F 74 6B CLR _g_ita_conv_table_lower$EIND ($746B)// clear (extended)
0x1F54 7E 1F FF JMP $1FFF (_coco3v_text_putc+0xC2)// jump (extended)
0x1F57 func: _coco3v_text_putc line: coco3_video.c(96):
    else if (c == '\n')
0x1F57 E6 E4 LDB , S // load b (indexed)
0x1F59 C1 0A CMPB #$0A // compare b (imm_byte)
0x1F5B 26 18 BNE $1F75 (_coco3v_text_putc+0x38)// branch if not equal (rel_byte)
0x1F5D func: _coco3v_text_putc line: coco3_video.c(98):
    if (++g_cursor_y == HIRES_TEXT_TOTAL_LINES)
0x1F5D F6 74 6C LDB $746C (_g_ita_conv_table_lower$EIND+0x1)// load b (extended)
0x1F60 5C IICB // increment b (implied)
0x1F61 F7 74 6C STB $746C (_g_ita_conv_table_lower$EIND+0x1)// store b (extended)
0x1F64 C1 18 CMPB #$18 // compare b (imm_byte)
0x1F66 10 26 00 95 LBNE $1FFF (_coco3v_text_putc+0xC2)// long branch if not equal (rel_word)
0x1F6A func: _coco3v_text_putc line: coco3_video.c(100):
    g_cursor_y--;
0x1F6A C6 17 LDB #$17 // load b (imm_byte)
0x1F6C F7 74 6C STB $746C (_g_ita_conv_table_lower$EIND+0x1)// store b (extended)
0x1F6F func: _coco3v_text_putc line: coco3_video.c(101):
    coco3v_text_scroll();
0x1F6F BD 1E DD JSR _coco3v_text_scroll ($1EDD)// jump to subroutine (extended)
0x1F72 7E 1F FF JMP $1FFF (_coco3v_text_putc+0xC2)// jump (extended)
0x1F75 func: _coco3v_text_putc line: coco3_video.c(105):
    else if (c == '\b')
0x1F75 C1 08 CMPB #$08 // compare b (imm_byte)
0x1F77 26 0E BNE $1F87 (_coco3v_text_putc+0x4A)// branch if not equal (rel_byte)
0x1F79 func: _coco3v_text_putc line: coco3_video.c(107):
    if (g_cursor_x)
0x1F79 F6 74 6B LDB _g_ita_conv_table_lower$EIND ($746B)// load b (extended)
0x1F7C 10 00 ??? ???
>R
A: 0x00, Signed: 0, Unsigned: 0, 0b00000000
B: 0x20, Signed: 32, Unsigned: 32, 0b00100000
D: 0x0020, Signed: 32, Unsigned: 32, 0b000000000100000, $0020 (m15+0x11)
E: 0x00, Signed: 0, Unsigned: 0, 0b00000000
F: 0x00, Signed: 0, Unsigned: 0, 0b00000000
V: 0x0000, Signed: 0, Unsigned: 0, 0b0000000000000000, m0 ($0000)
X: 0x0000, Signed: 0, Unsigned: 0, 0b0000000000000000, m0 ($0000)
Y: 0x0346, Signed: 838, Unsigned: 838, 0b0000001101000110, $0346 (_count_bits$EIND+0xE)
S: 0x70B5, Signed: 28853, Unsigned: 28853, 0b0111000010110101, $70B5 (_g_rand_seed+0x7B4)
U: 0x2005, Signed: 8197, Unsigned: 8197, 0b0010000000000101, _tprintf_text_putc ($2005)
V: 0x4010, Signed: -24560, Unsigned: 40976, 0b1010000000010000, $4010 (s_vector+0x2020)
PC: 0x1F3D, Unsigned: 7997, _coco3v_text_putc ($1F3D)
DP: 0x7B, Unsigned: 123
CC: 0x91, Carry: 1, Overflow: 0, Zero: 0, Negative: 0, IRQ: 1, HalfCarry: 0, FIRQ: 0, Entire: 1
>ds _math_divmodu_16_16_full
0x3844 EC 62 _math_divmodu_16_16_full:LDD 2, S // load d (indexed)
0x3846 10 83 00 01 CMPD #$0001 // compare d (imm_word)
0x384A 23 13 BLS $385F (_math_divmodu_16_16_full+0x1B)// branch if lower or same (unsigned) (rel_byte)
0x384C 10 83 80 00 CMPD #$8000 // compare d (imm_word)
0x3850 24 13 BCC $3865 (_math_divmodu_16_16_full+0x21)// branch if carry clear, or branch if high or eq
0x3852 1F 16 TFR X, W // transfer (reg_post)
0x3854 10 4F CLRD // clear d (implied)
0x3856 11 AE 62 DIVQ 2, S // 6309 quad divide (indexed)
0x3859 ED F8 04 STD [4, S] // store d (indexed)
0x385C 1F 61 TFR W, X // transfer (reg_post)
0x385E 39 RTS // return from subroutine (implied)
0x385F 10 4F CLRD // clear d (implied)
0x3861 ED F8 04 STD [4, S] // store d (indexed)
0x3864 39 RTS // return from subroutine (implied)
0x3865 AF F8 04 STX [4, S] // store x (indexed)
0x3868 10 32 01 SUBR D, X // sub register (reg_post)
0x386B 24 04 BCC $3871 (_math_divmodu_16_16_full+0x2D)// branch if carry clear, or branch if high or eq
0x386D 8E 00 00 LDX #$0000 // load x (imm_word)
0x3870 39 RTS // return from subroutine (implied)
0x3871 AF F8 04 STX [4, S] // store x (indexed)
0x3874 8E 00 01 LDX #$0001 // load x (imm_word)
0x3877 39 RTS // return from subroutine (implied)
0x3878 func: ? line: _umoddi3.c(1135):
0x3878 7A 00 FF DEC $00FF (m15+0xF0) // decrement (extended)
0x387B 10 EF 9F 00 FE STS [$00FE (m15+0xEF)] // store s (indexed)
0x3880 34 60 PSHS U, Y // push to s (sys_post)
0x3882 32 E8 00 LEAS 0, S // load effective address s (indexed)
<monitor> OK

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